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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): TADA et al.

Serial No.: 10/814,803

Filed: April 1, 2004

Title: MULTI-LAYER PRINTED CIRCUIT BOARD
AND METHOD FOR MANUFACTURING THE
SAME

Atty. Dkt.: 01-627

Group Art Unit: 2841

Examiner: Unknown



Commissioner for Patents
Alexandria, VA 22313

Date: June 6, 2006

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Restriction Requirement mailed on April 19, 2006 in connection with the above application, Applicants hereby elect Group I of claims 1-13, drawn to a printed circuit board.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

David G. Posz
Reg. No. 37,701

Posz Law Group, PLC
12040 South Lakes Drive
Suite 101
Reston, VA 20191
(703) 707-9110
Customer No. 23400